

FIG. 1

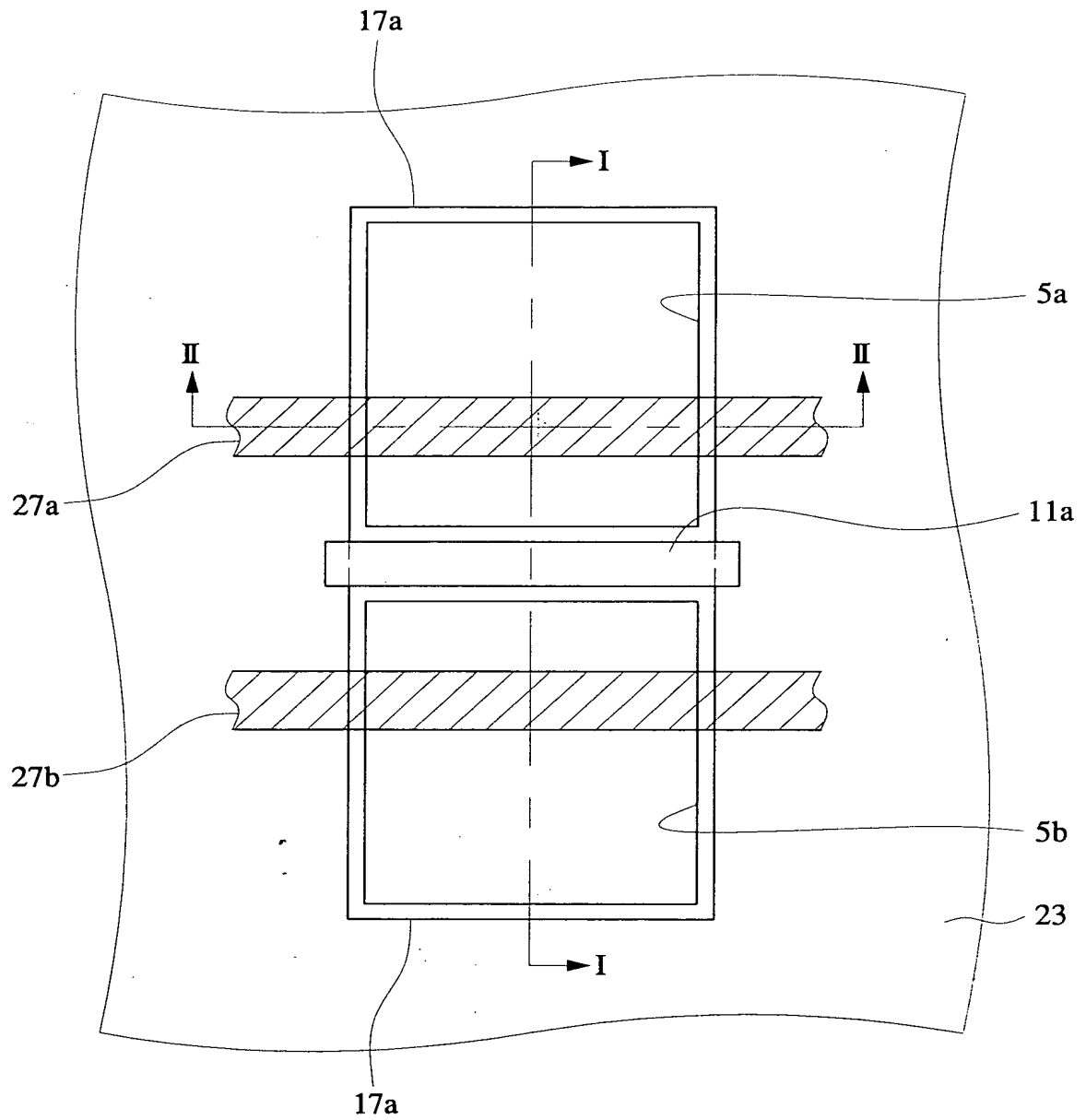


FIG. 2A

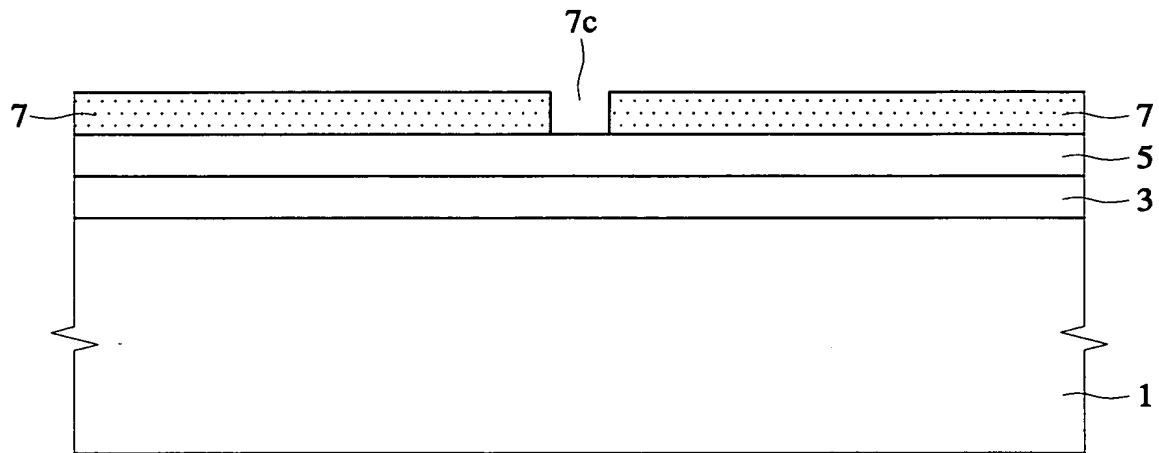


FIG. 2B

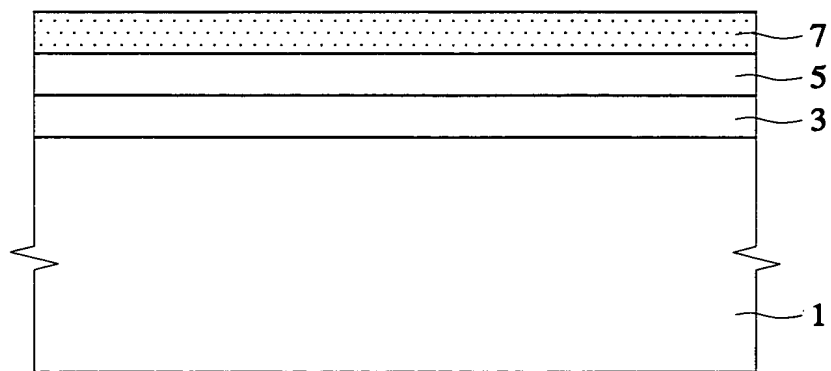


FIG. 3A

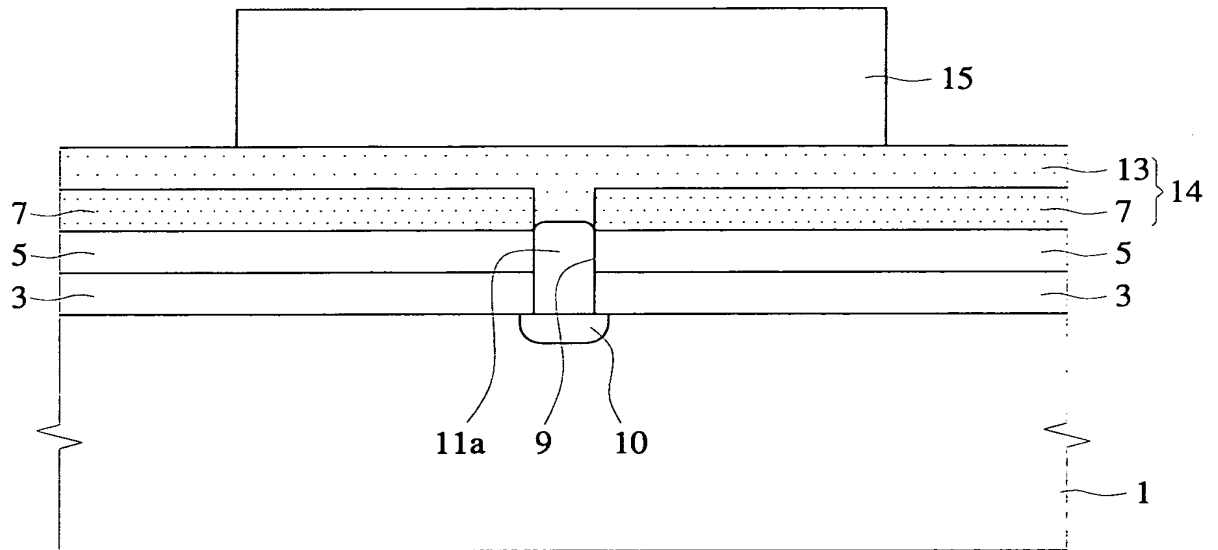


FIG. 3B

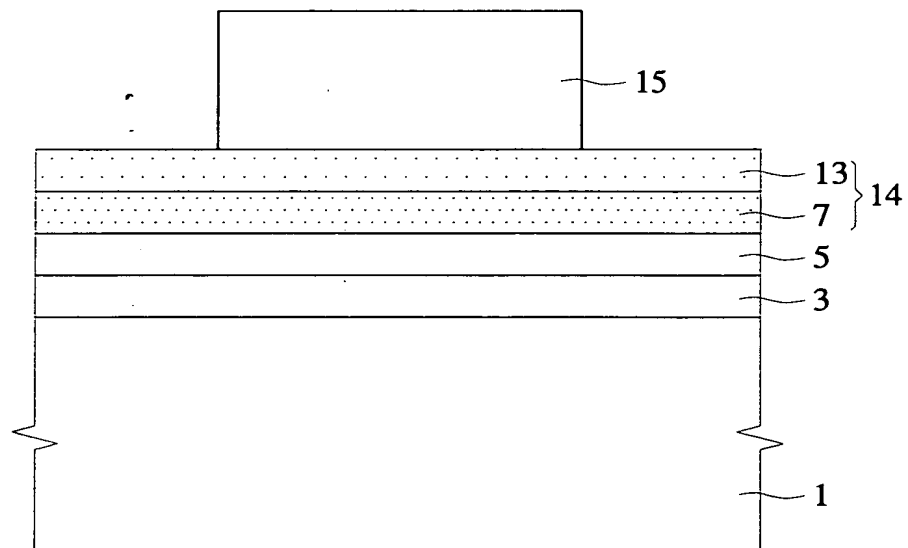


FIG. 4A

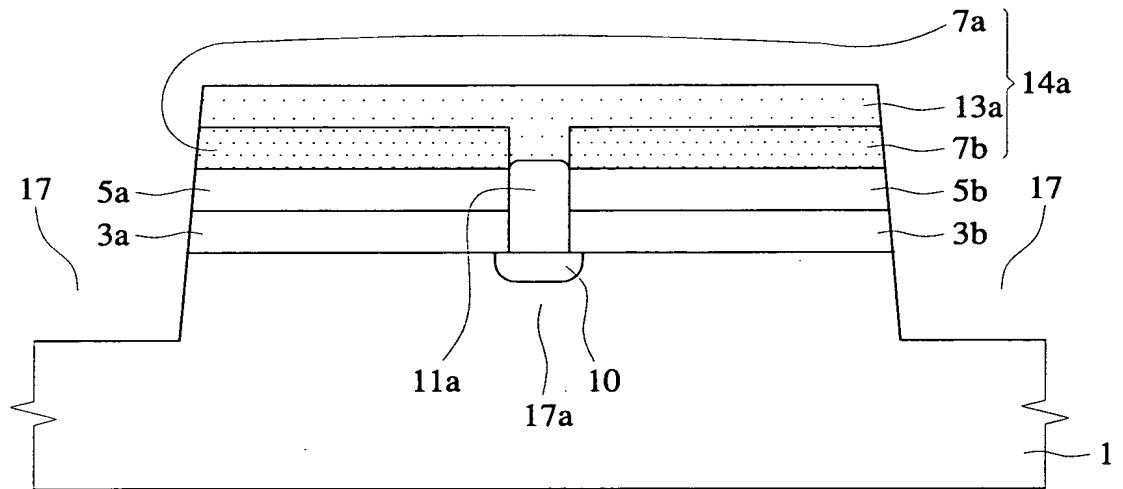


FIG. 4B

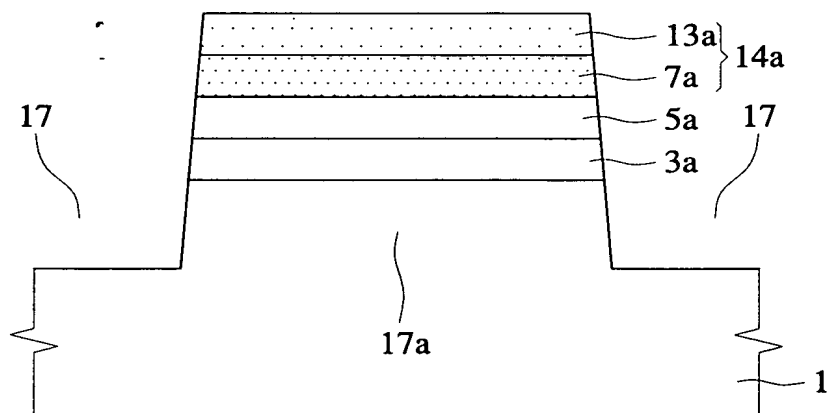


FIG. 5A

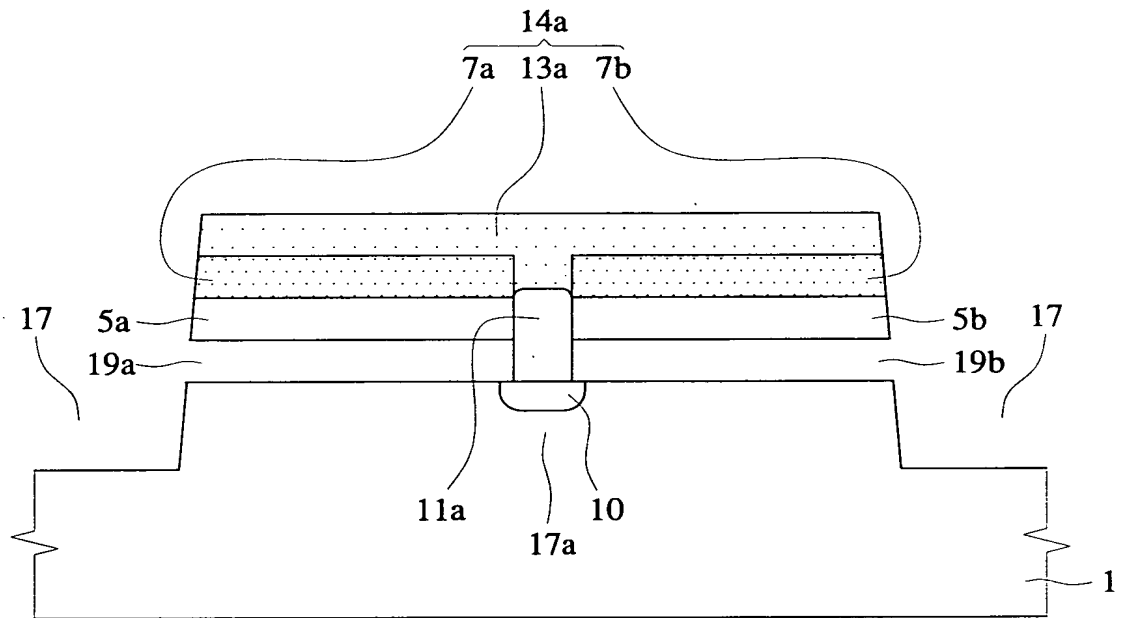


FIG. 5B

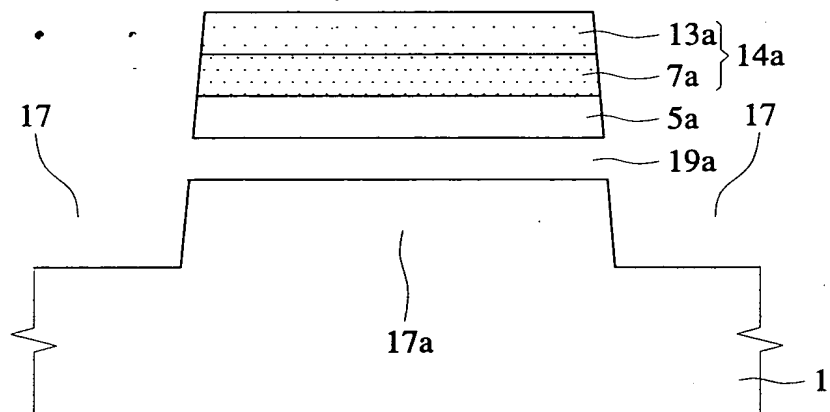


FIG. 6A

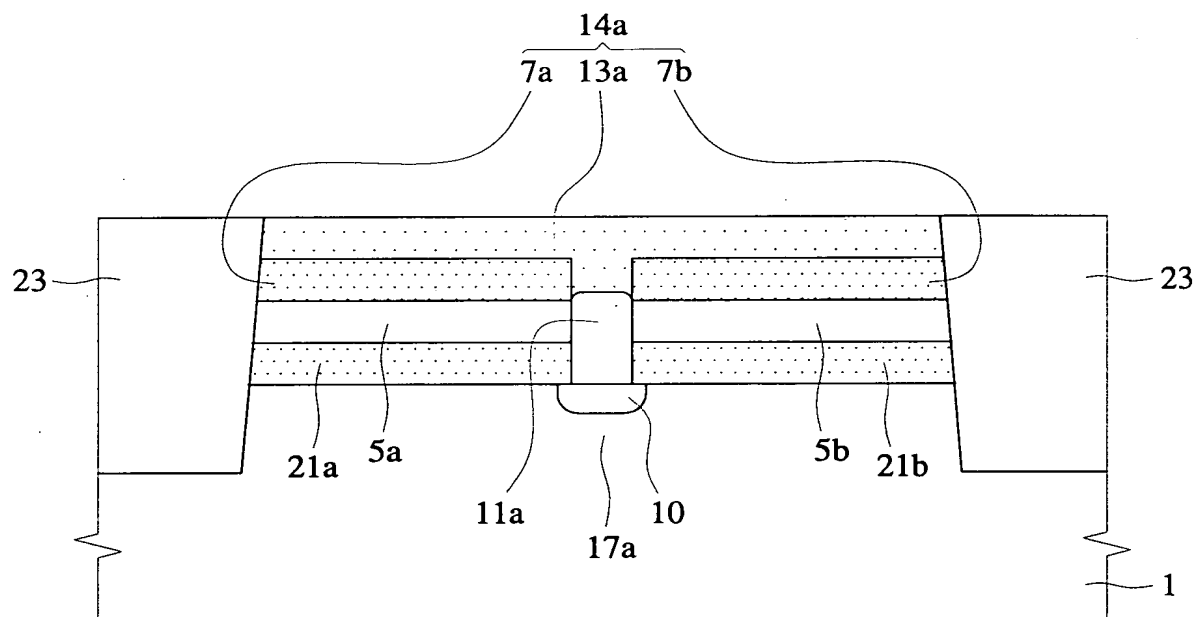


FIG. 6B

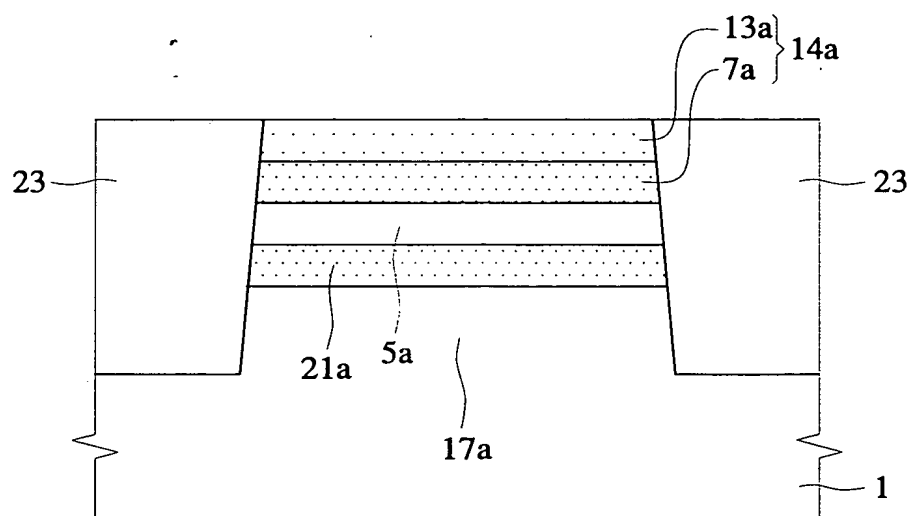


FIG. 7A

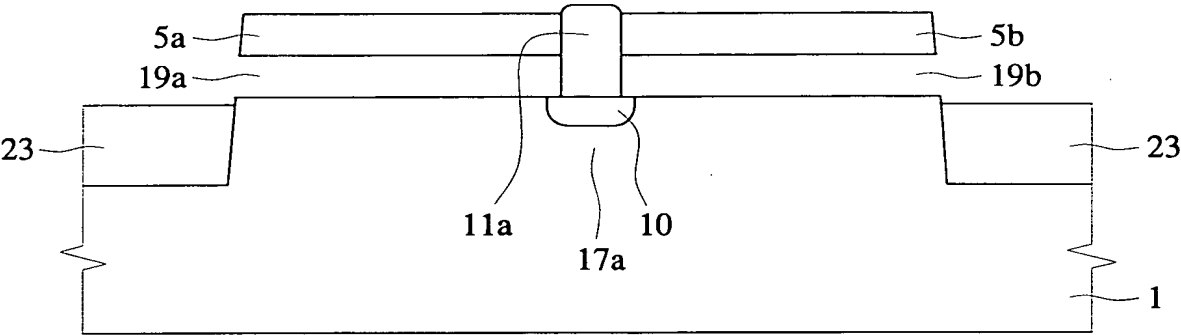


FIG. 7B

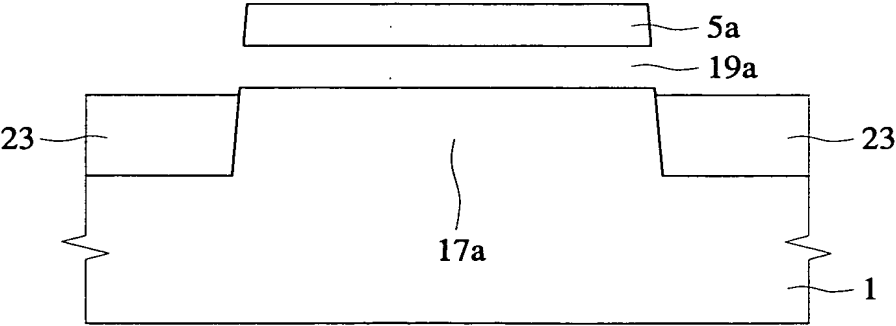


FIG. 8A

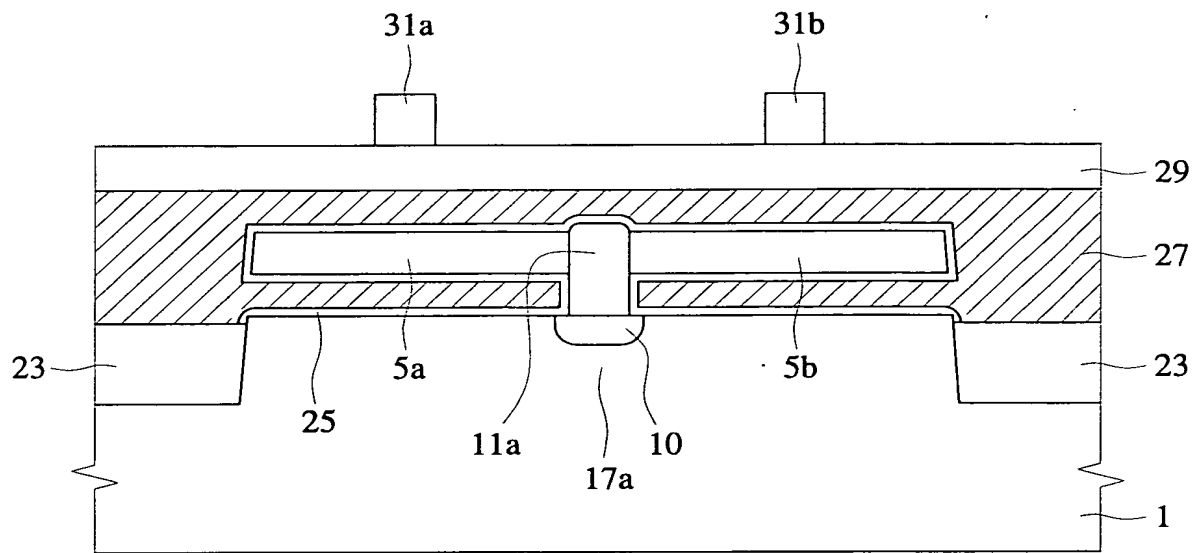


FIG. 8B

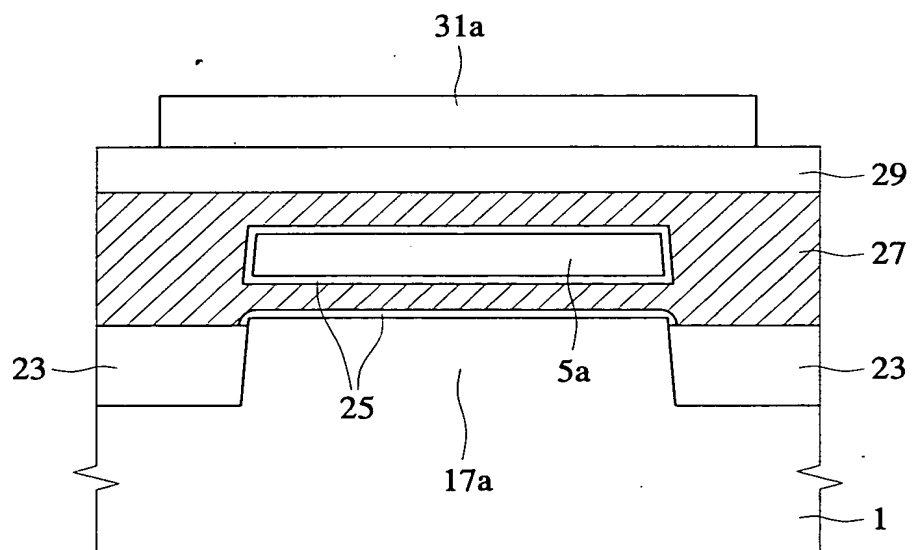


FIG. 9A

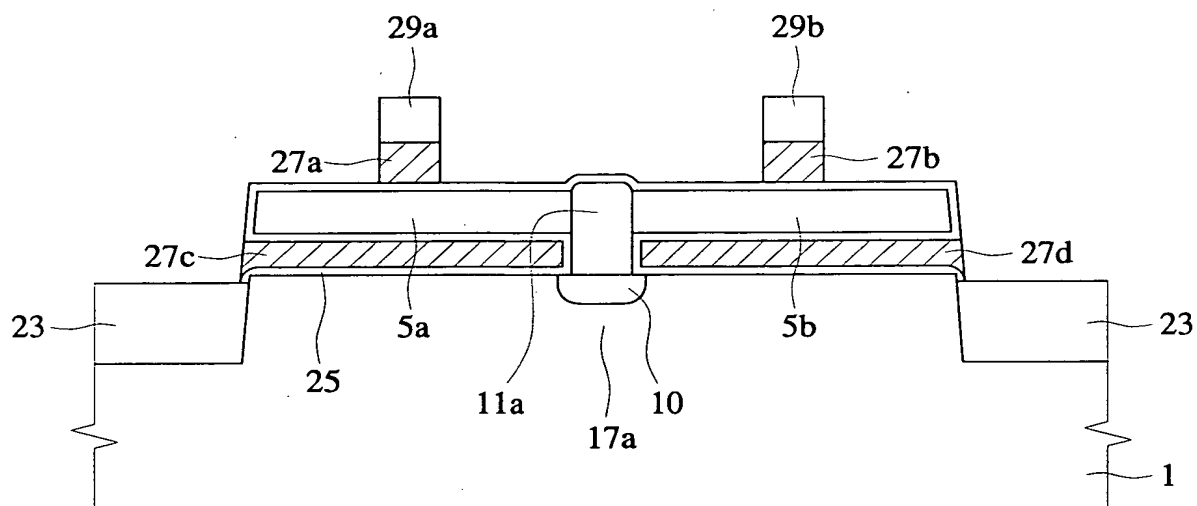
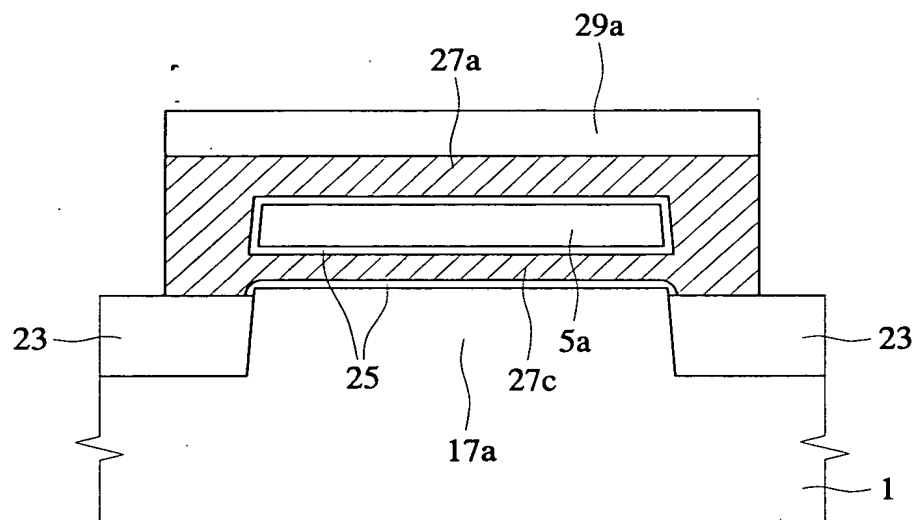


FIG. 9B



This cross-sectional view shows a semiconductor device with two active regions, 29a and 29b, separated by a central region 10. The device is built on a substrate 23. Each active region contains a gate stack 27a and 27b, respectively, which includes a gate dielectric 27c and a gate electrode 27d. The gate stacks are surrounded by a gate spacer 33. The active regions are separated by a central region 10, which includes a central gate stack 11a and a central gate electrode 17a. The device is connected to a power supply 1 through a contact 35a and a contact 35b. The contact 35a is connected to the gate electrode 27d, and the contact 35b is connected to the gate electrode 17a. The device is also connected to a power supply 23 through a contact 37a and a contact 37b. The contact 37a is connected to the gate electrode 27c, and the contact 37b is connected to the gate electrode 17a. The device is also connected to a power supply 23 through a contact 35b and a contact 37b. The contact 35b is connected to the gate electrode 17a, and the contact 37b is connected to the gate electrode 27d.

Fig. 1 is a cross-sectional view of a semiconductor device. The device is formed on a substrate 1. A well 23 is formed in the substrate 1. A gate stack 25 is formed on the substrate 1, with a gate oxide 27a and a gate electrode 29a. A source/drain region 35b is formed on the substrate 1, with a source/drain oxide 27c and a source/drain electrode 5a. The device is connected to a power supply 1.

FIG. 10

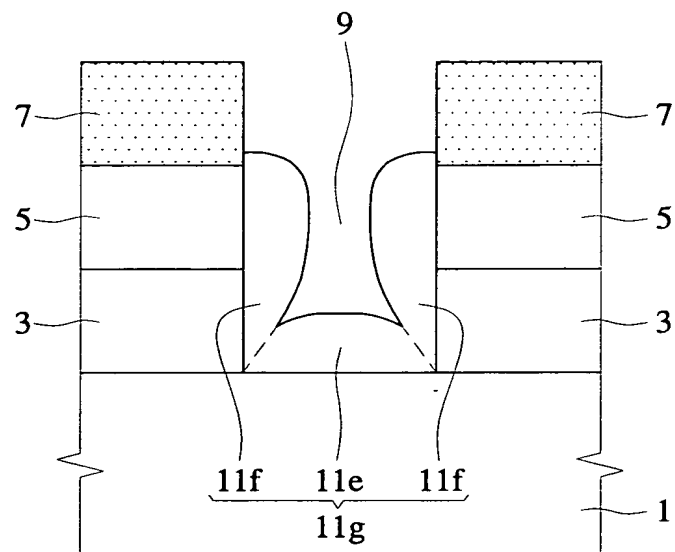


FIG. 11

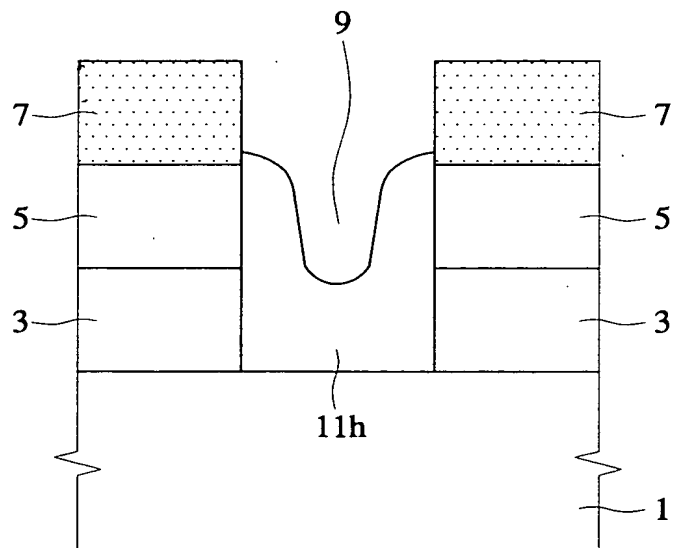


FIG. 13

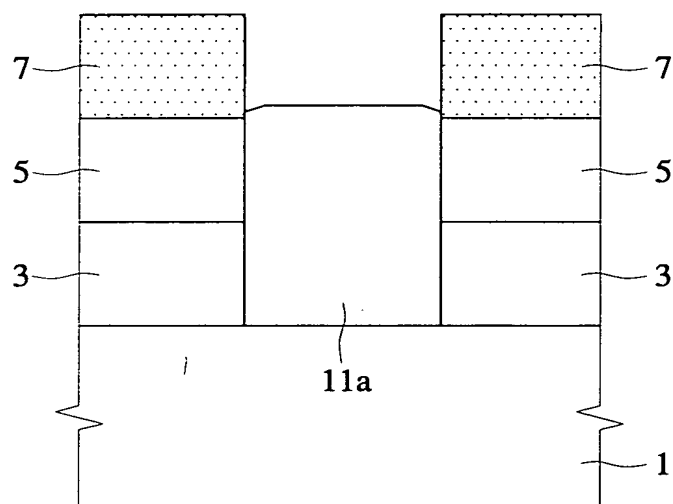


FIG. 14

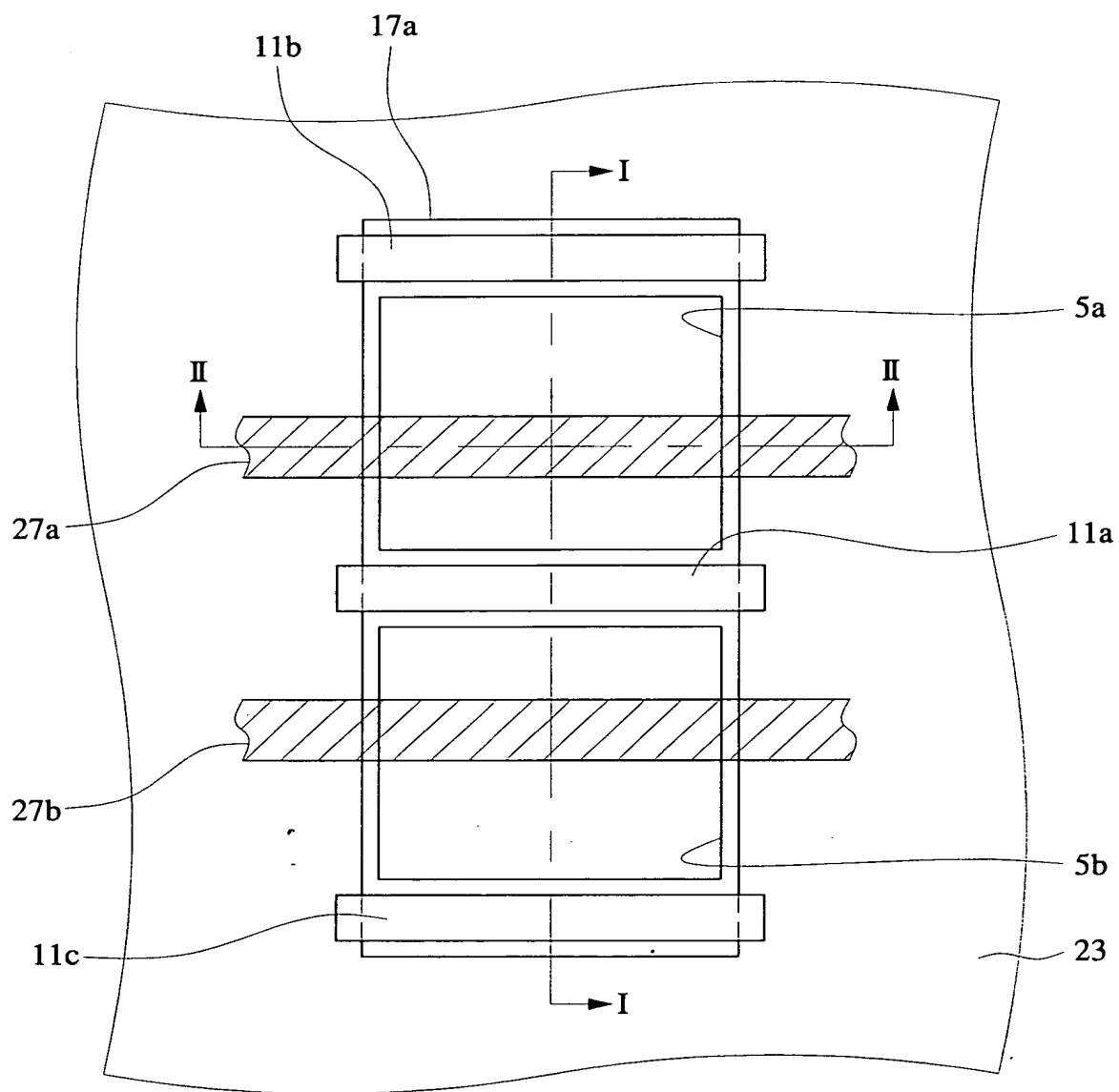


FIG. 15

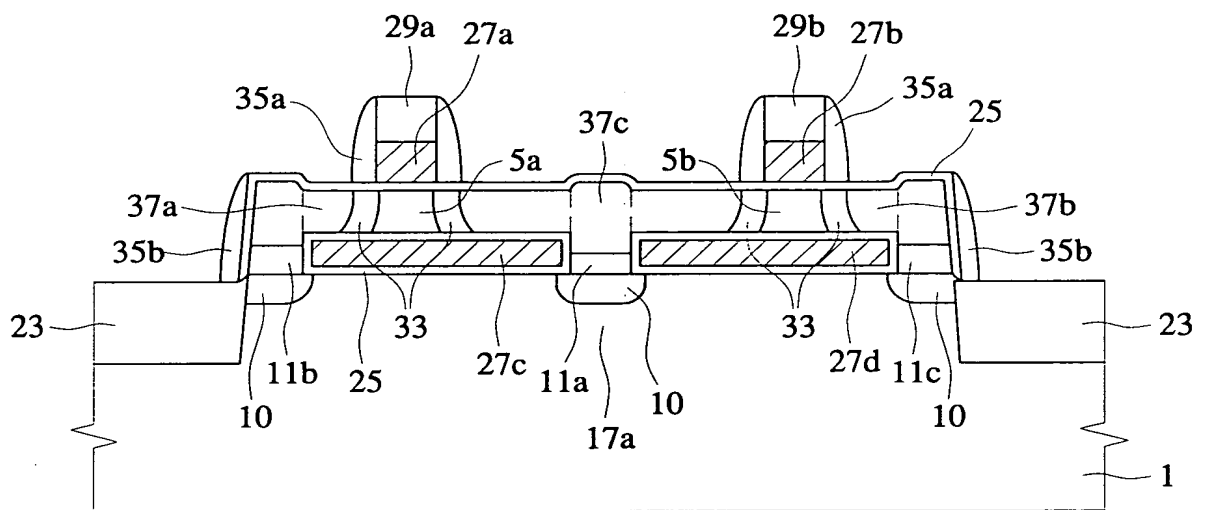


FIG. 16

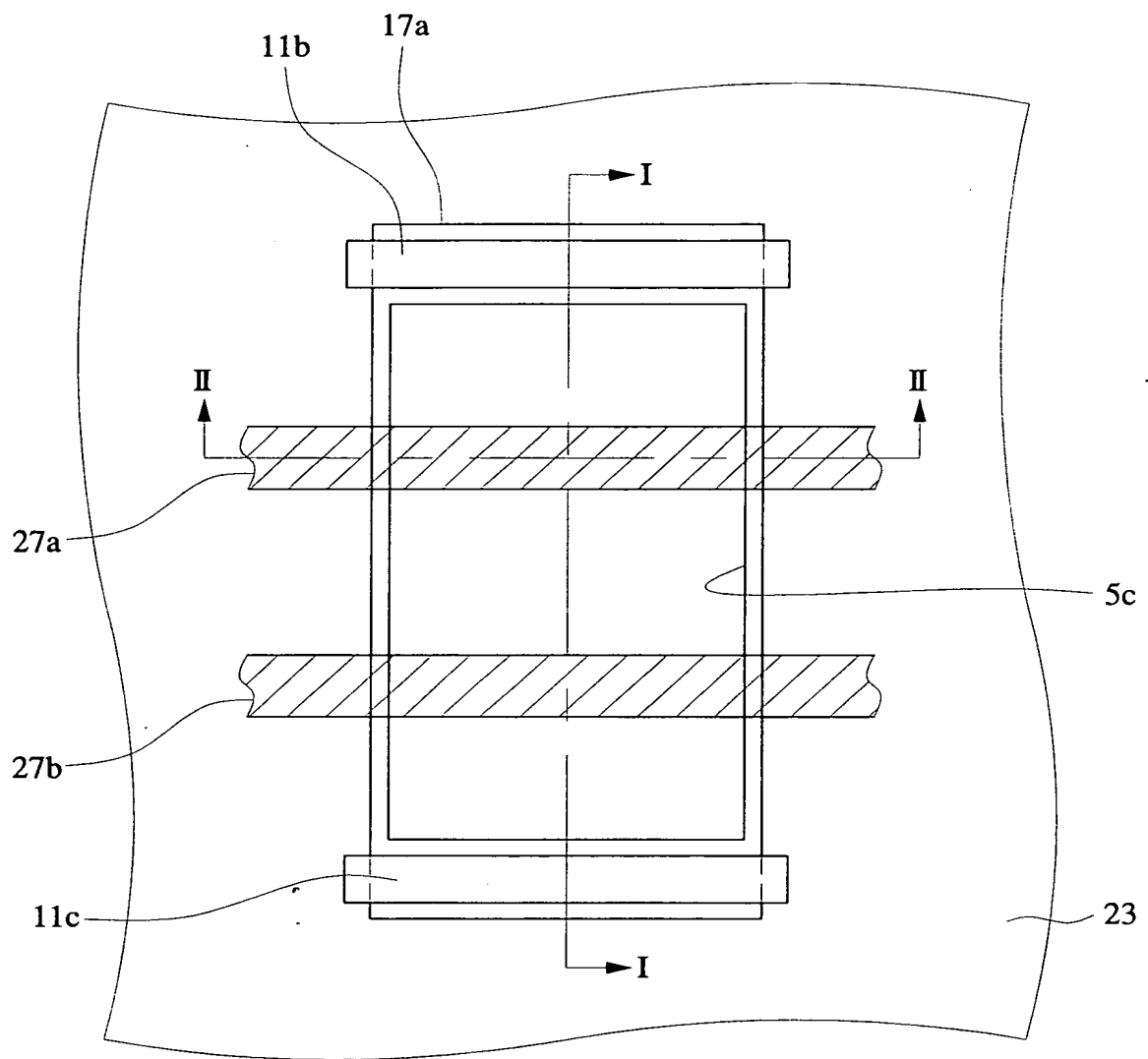


FIG. 17

